



PATENT APPLICATION

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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of:
Terry Lee GOODE

Serial Number: 09/841,974

Filed: April 24, 2001

For: EMULATOR WITH SWITCHING NETWORK
CONNECTIONS

Group Art Unit: TBA

Examiner: TBA

Attorney Docket No.: 003921.00011

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OCT 28 2002
Technology Center 2100

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J. Haym
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PRELIMINARY AMENDMENT

The Honorable Commissioner for Patents
Washington, D.C. 20231

Sir:

Prior to initial examination in the above-captioned Application, please amend the application as follows:

IN THE SPECIFICATION:

Please replace the paragraph beginning at page 1, line 3, with the following rewritten paragraph:

--1. Field of the Invention--

Please replace the paragraph beginning at line 11, page 1, with the following rewritten paragraph:

-- Hardware emulation of integrated circuits is a method extensively used in the design of complex logic circuits. A hardware emulator provides a substrate for implementing a complex logic circuit. Typically, the emulator includes as building blocks of the substrate one or more interconnected circuit boards. In each circuit board, a number of interconnected programmable logic devices are provided for implementing selected portions of the complex logic circuit. Field programmable gate arrays ("FPGAs") are the most common programmable logic devices ("PLDs") found in a hardware emulator. The hardware emulator is controlled by software running on a host processor, such as an engineering workstation.--

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